

FIG. 1

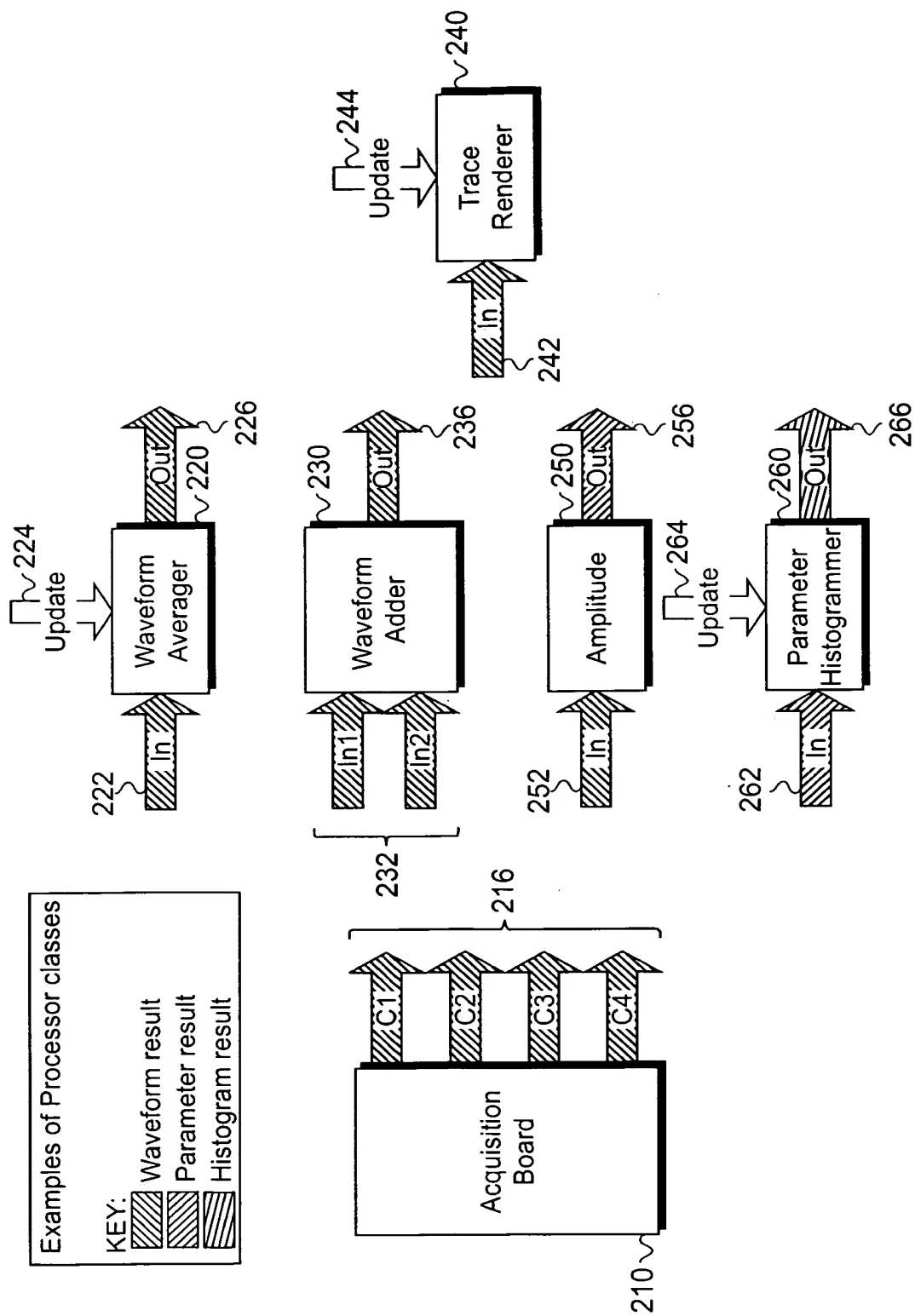


FIG. 2

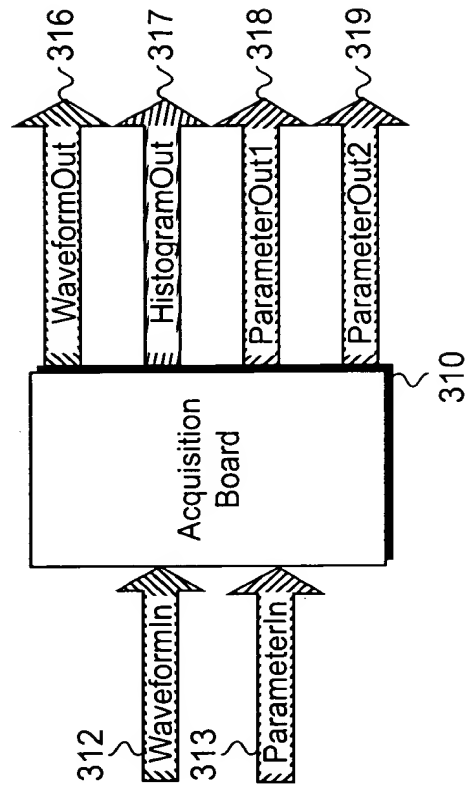


FIG. 3

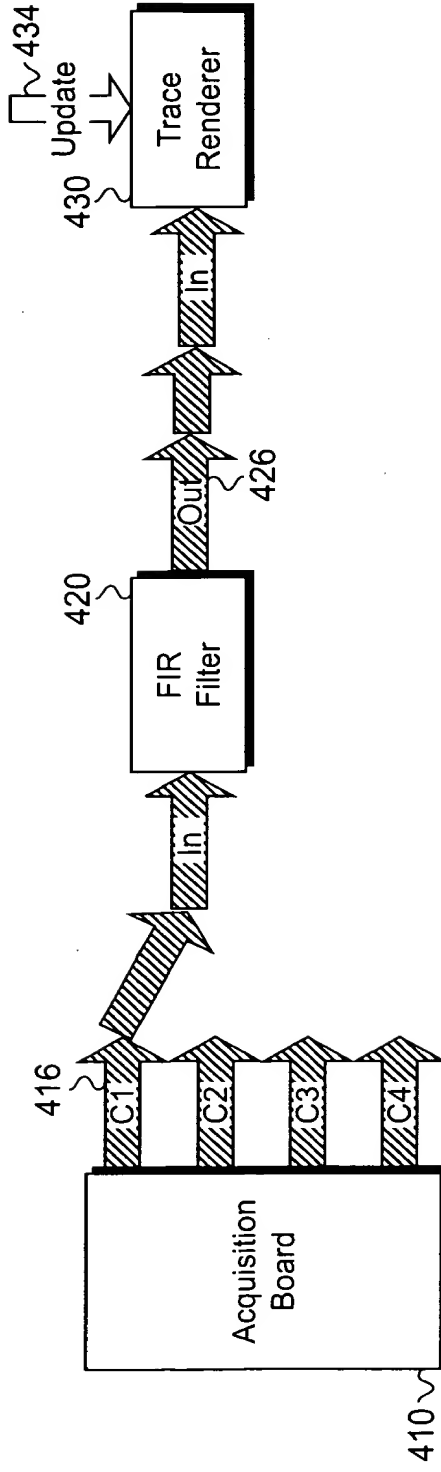


FIG. 4

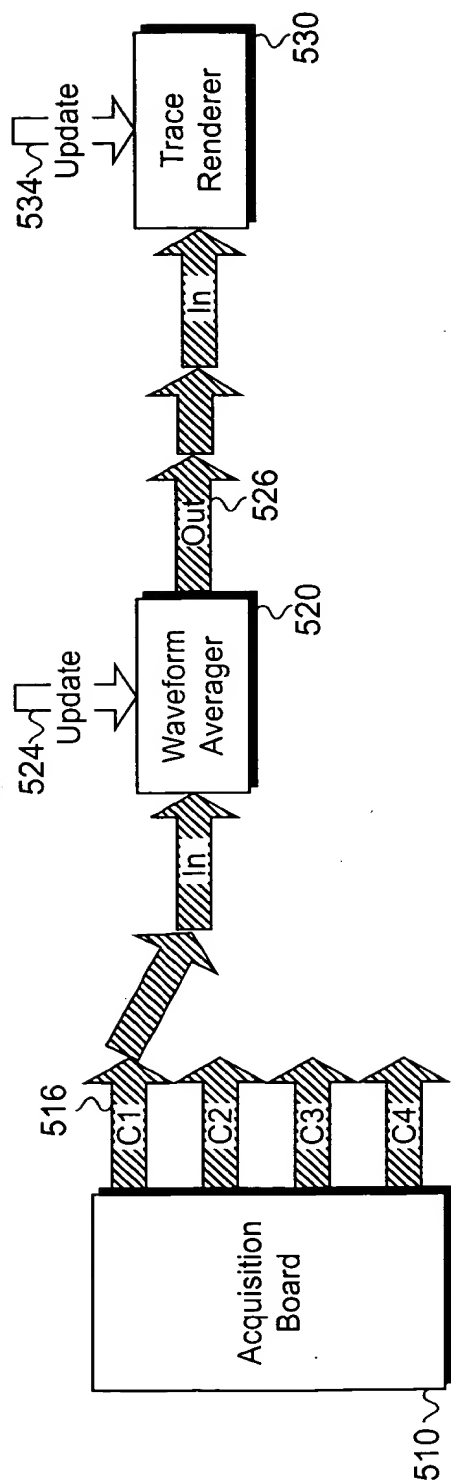


FIG. 5

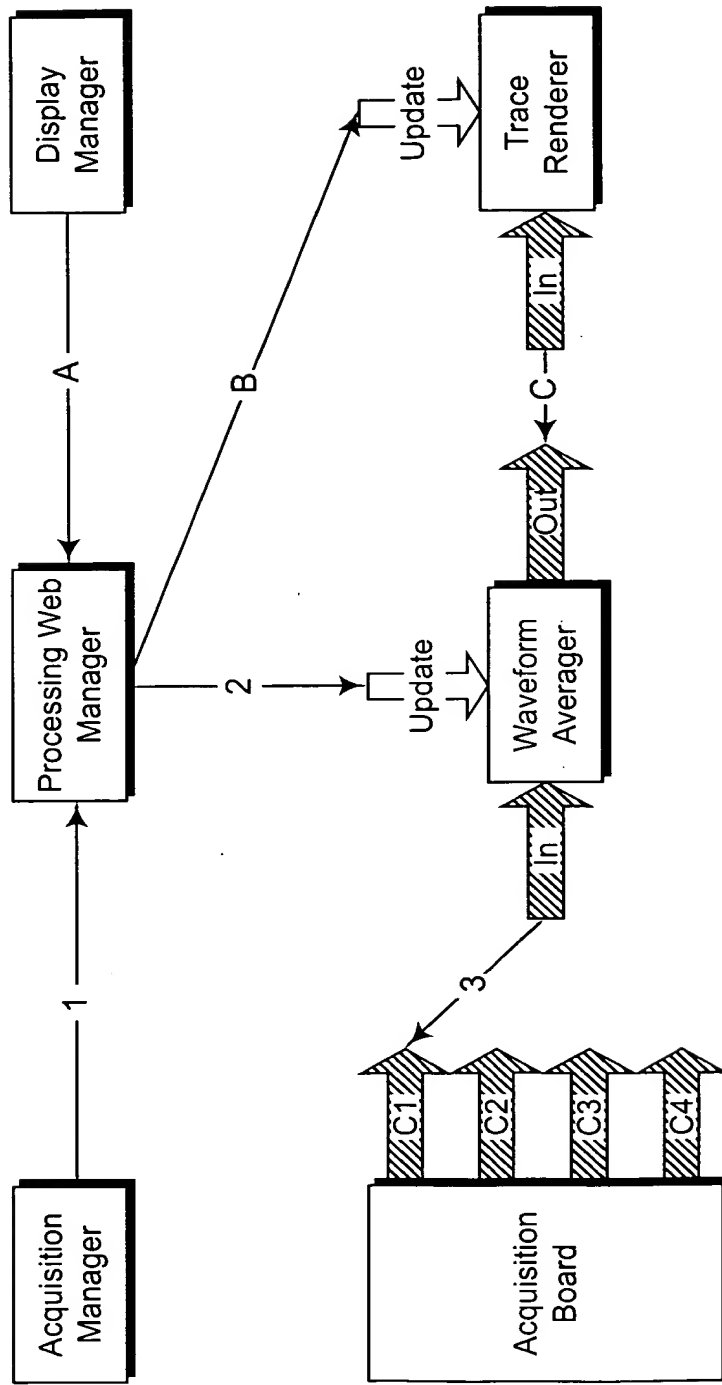


FIG. 6

The diagram illustrates a multi-view system architecture. On the left, there are two columns of source nodes: 'plugin A' (A1, A2, A3, A4) and 'plugin B' (B1, B2, B3, B4). Below these are eight main source nodes (M1 through M8) and a 'Parameters' line (line 1 to 20 view). On the right, there are eight 'View' nodes (T1 view through T8 view). Arrows indicate data flow from sources to views. A thick arrow points from B1 to T1 view. A text box labeled '2 functions with 3 sources for each of 8 viewable traces' points to the connections between B2, B3, and B4 and the views. The 'Parameters' line is shown at the bottom right, with a note 'Parameters line 1 to 20 view (as text w/ icon)'.

Note: persistence
in 7200 is a
display object

FIG. 7A

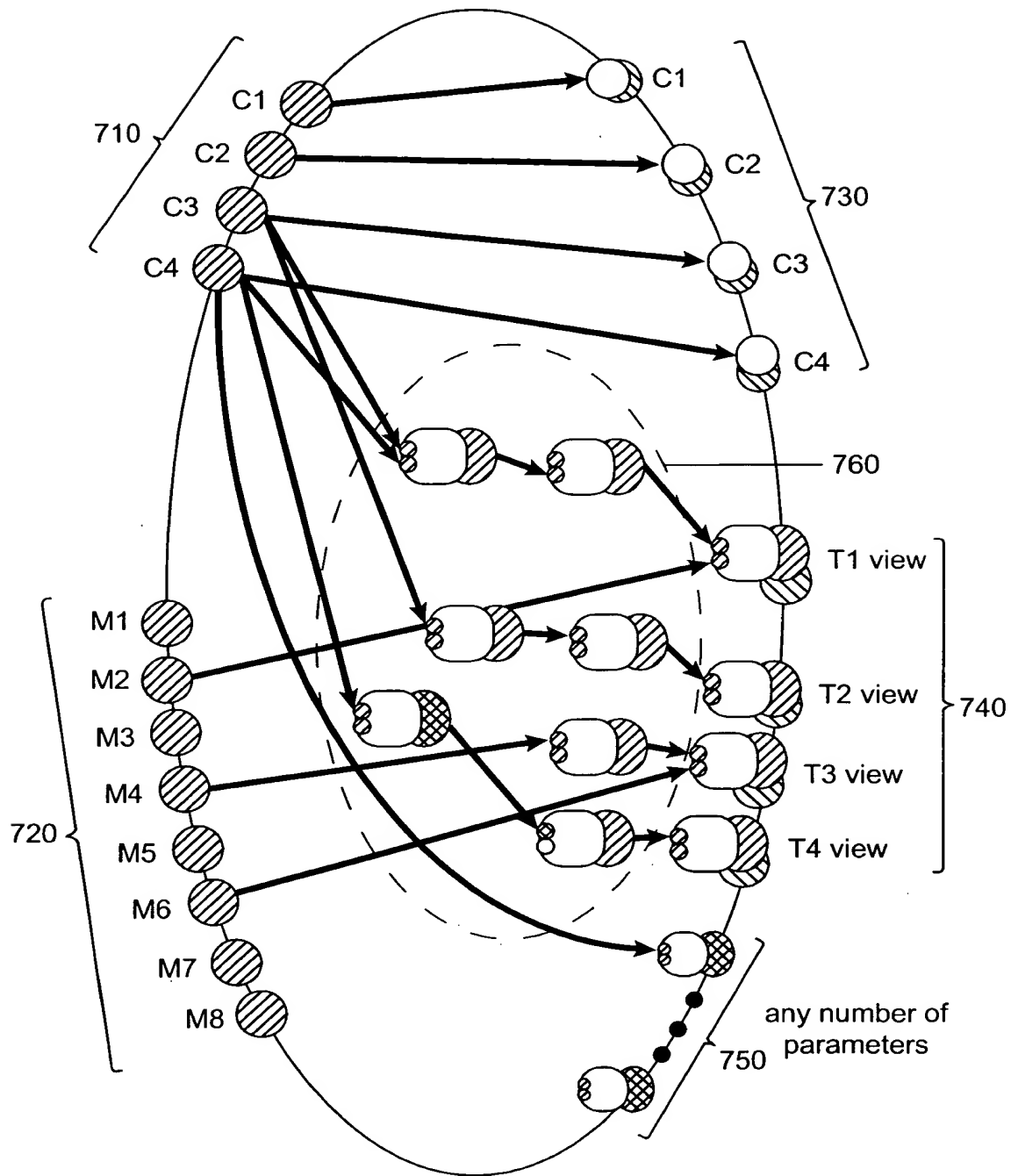
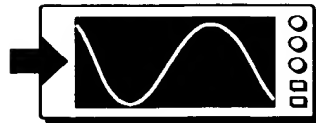
[illegible]

FIG. 7B



Parameter Result



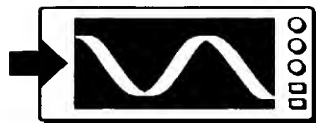
Waveform Result



Histogram Result



XY Result



Persistence Result



Pass/Fail Result

FIG. 8

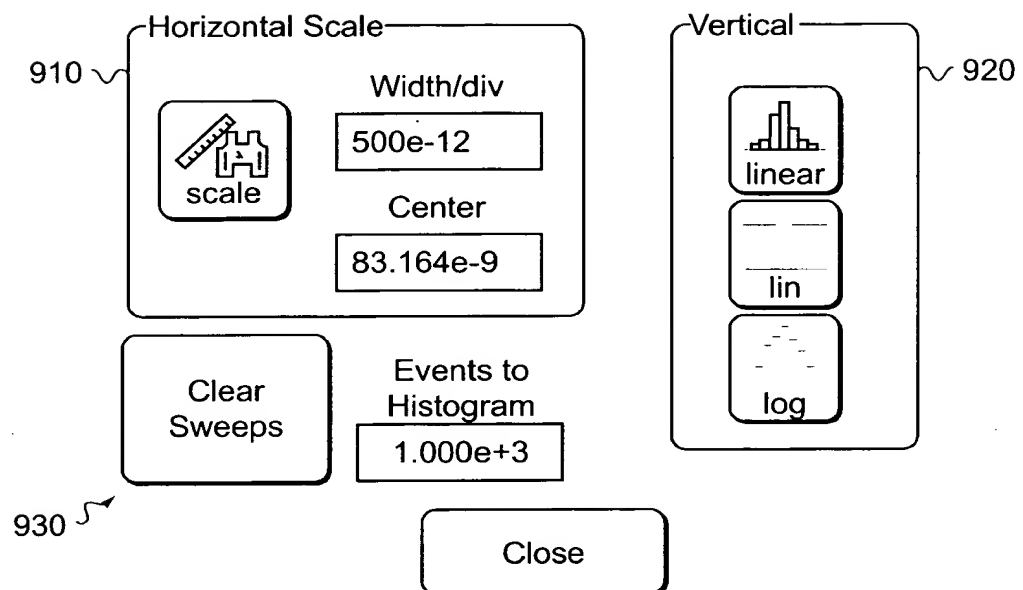


FIG. 9

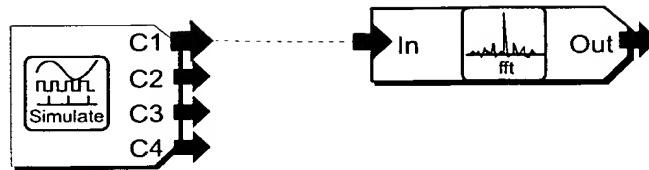


FIG. 10A

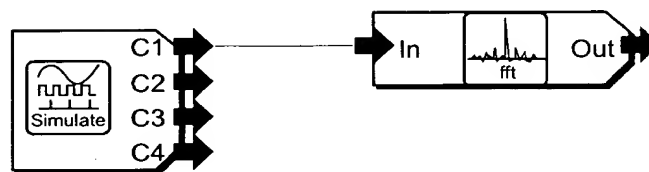


FIG. 10B

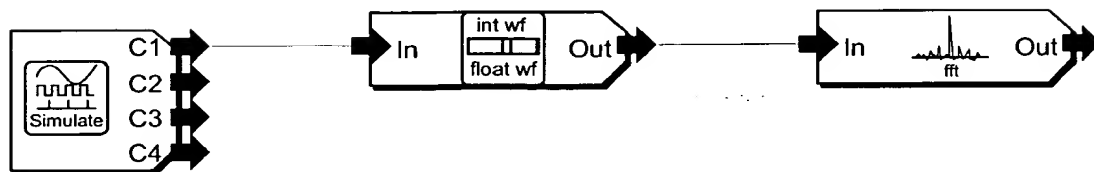


FIG. 10C

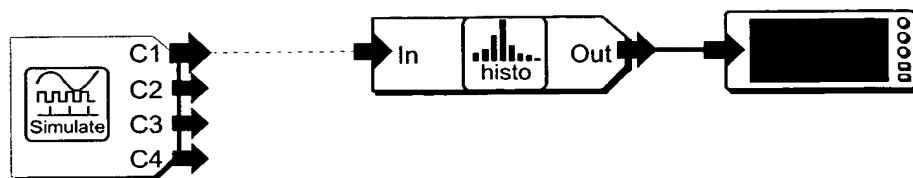


FIG. 11A

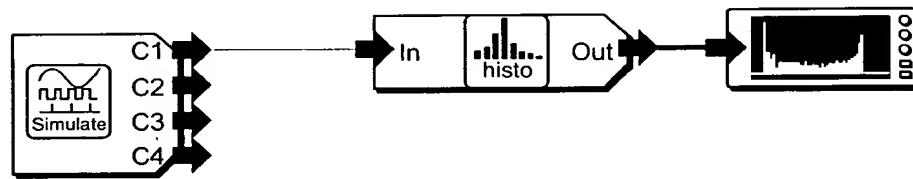


FIG. 11B

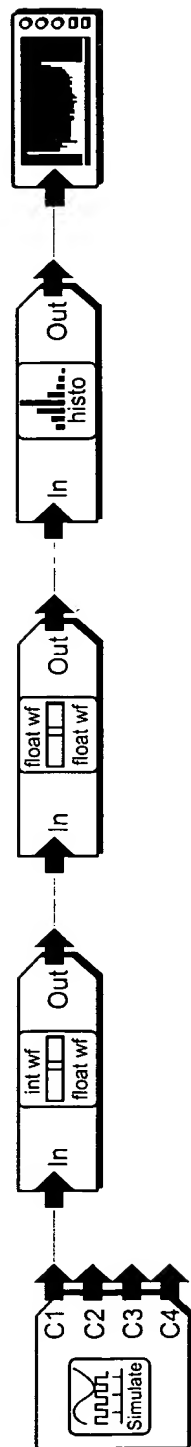


FIG. 11C

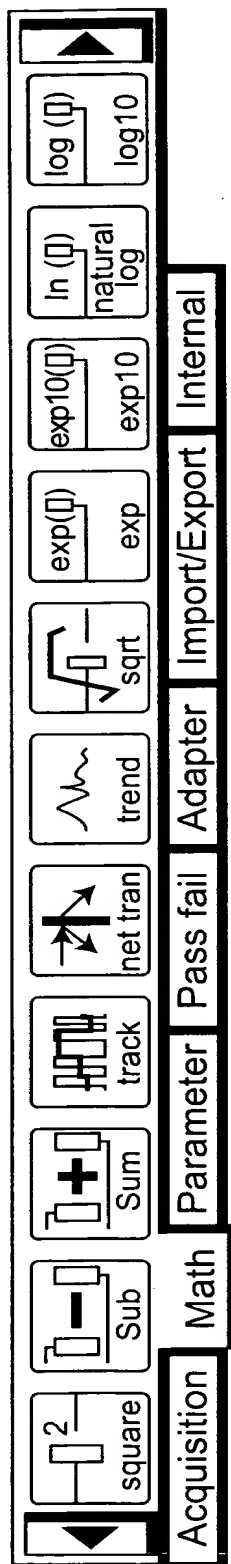


FIG. 12

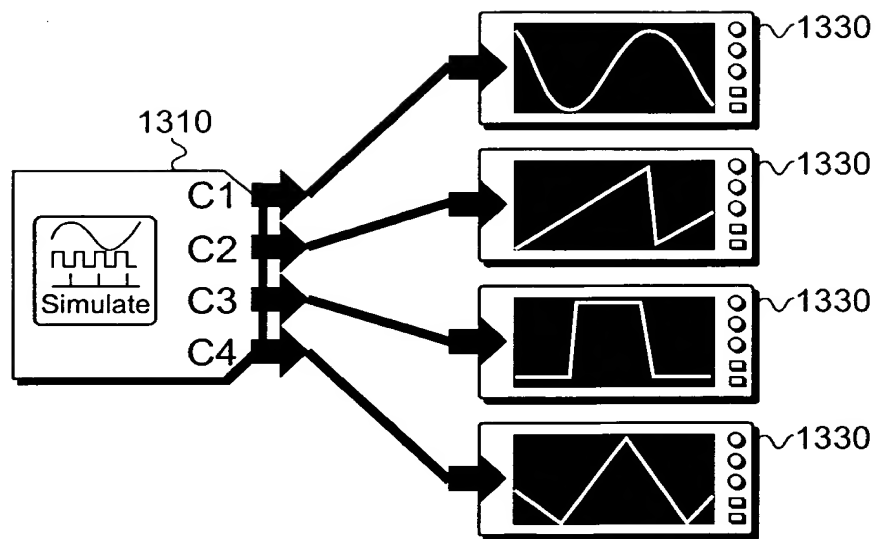


FIG. 13

Figure 1 is a block diagram of a signal processing system. A 'Simulate' block (1310) outputs four signals (C1, C2, C3, C4) to four separate display units (1330). Each display unit shows a different waveform: C1 shows a sine wave, C2 shows a sawtooth wave, C3 shows a square wave, and C4 shows a triangular wave. The output of the 'Simulate' block is also connected to a '1450' block, which is an 'ampl' (amplifier) block. The output of the 'ampl' block is connected to a '1460' block, which displays '287.7 mV'.

1. *Journal of the American Medical Association*, 1997; 277: 1025-1030.

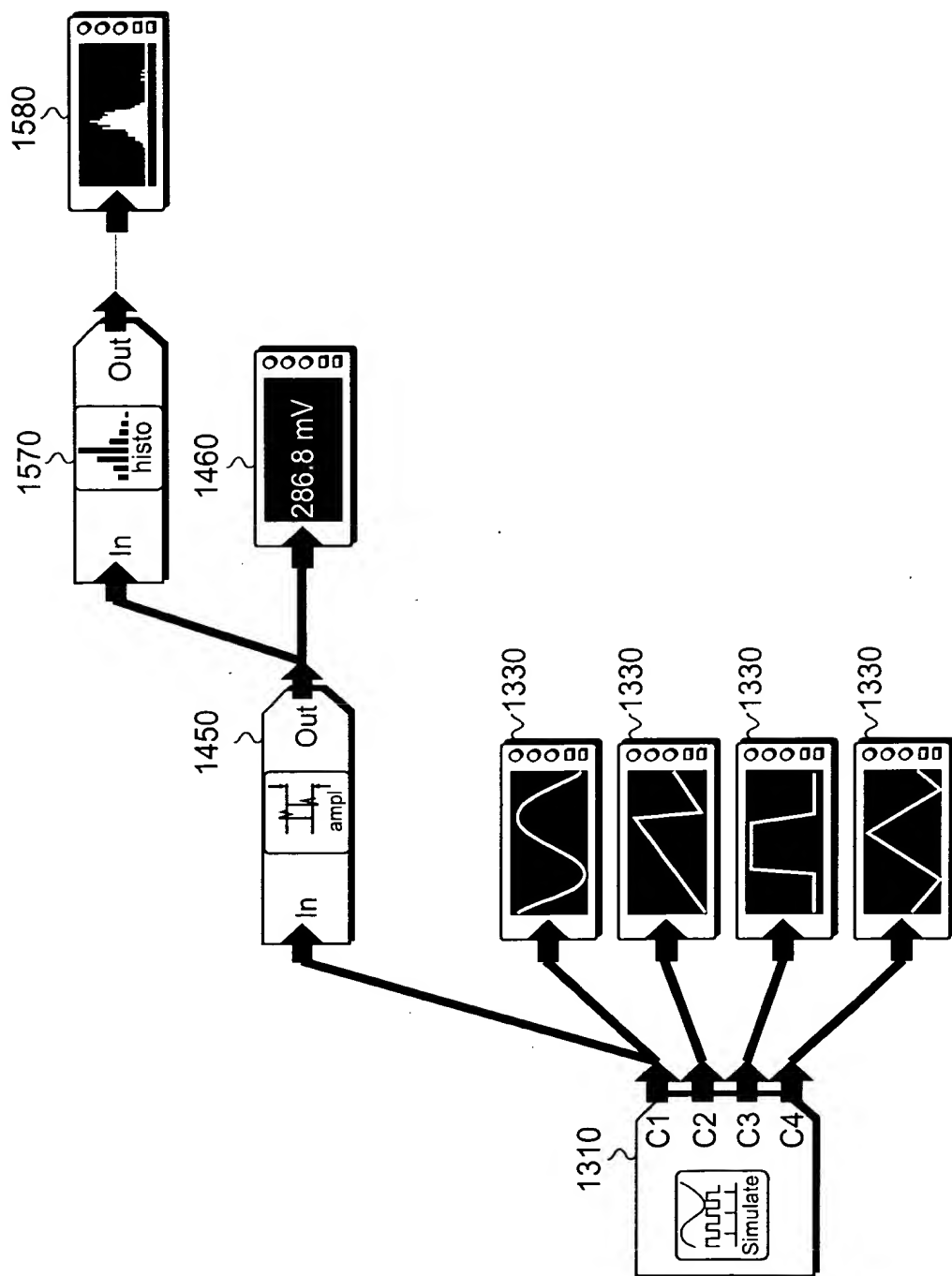


FIG. 15

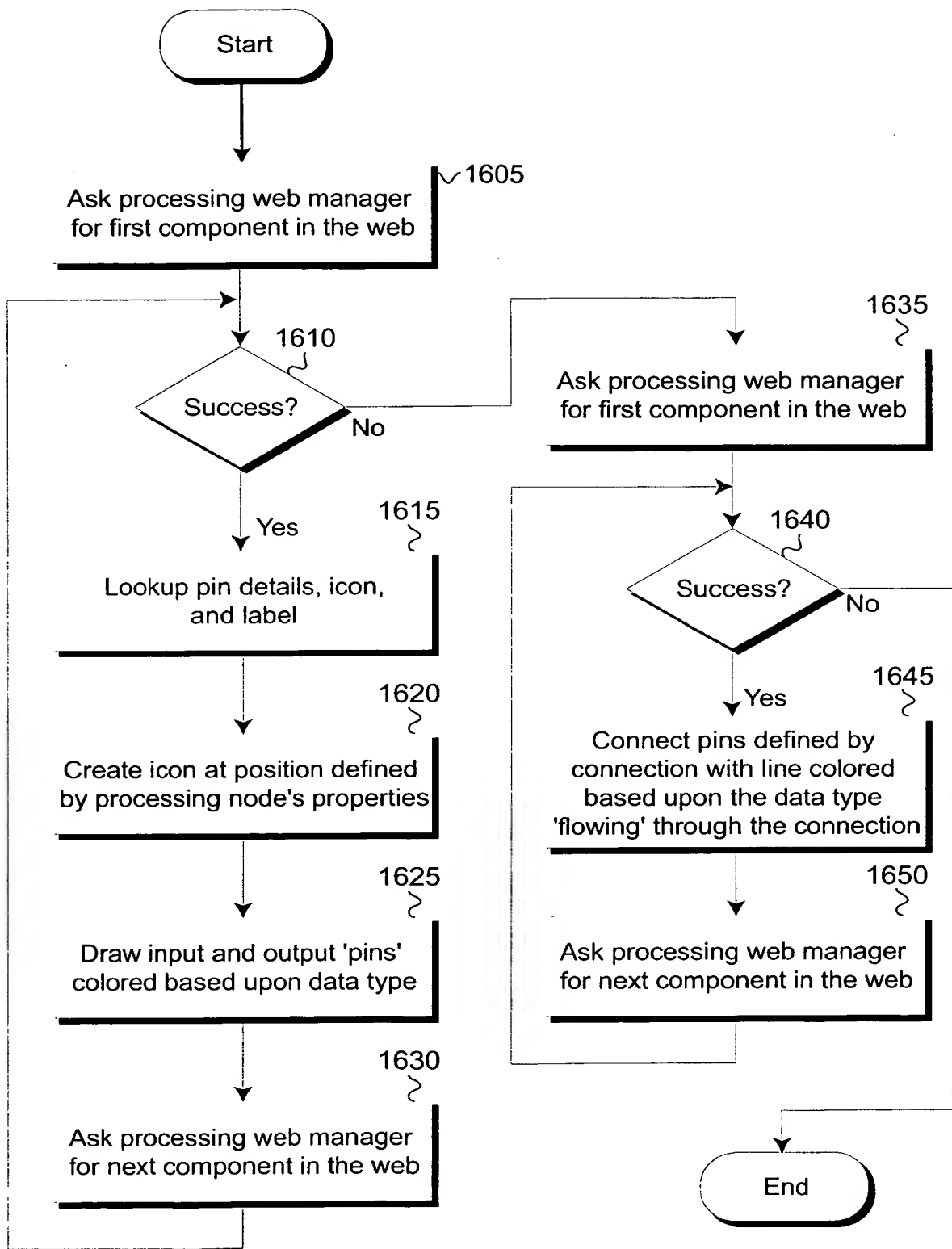


FIG. 16

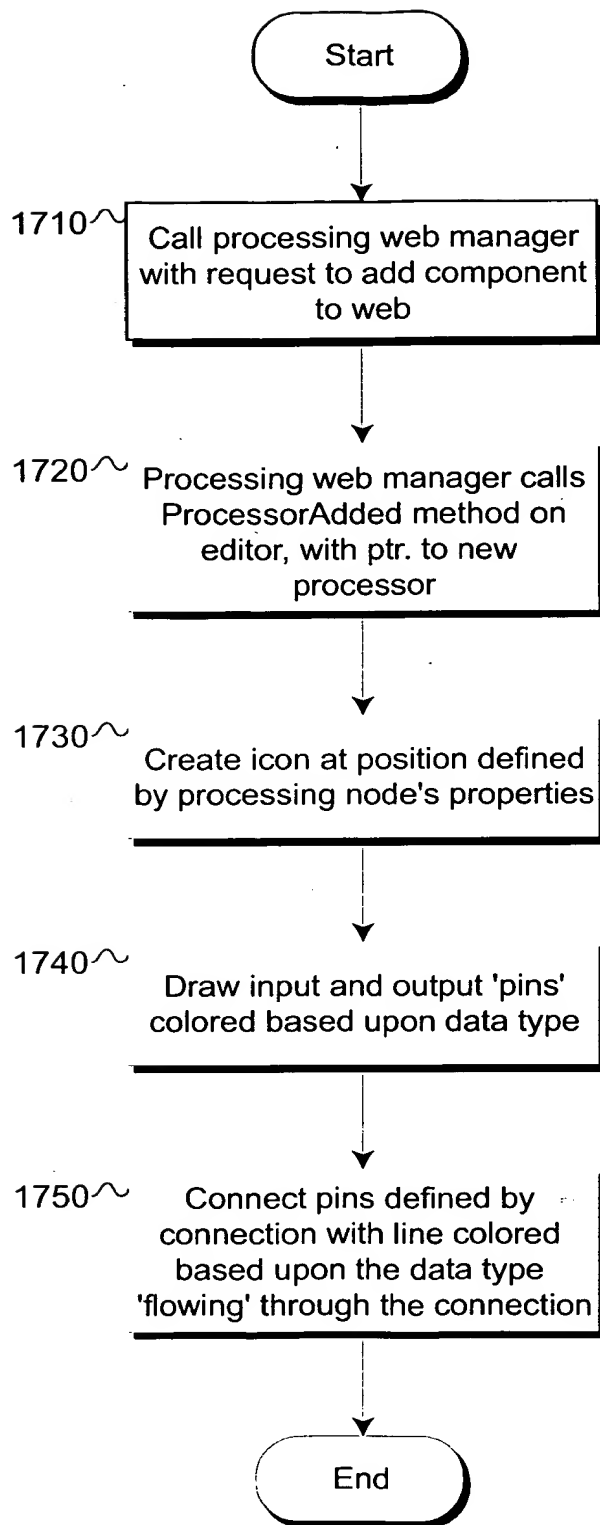


FIG. 17

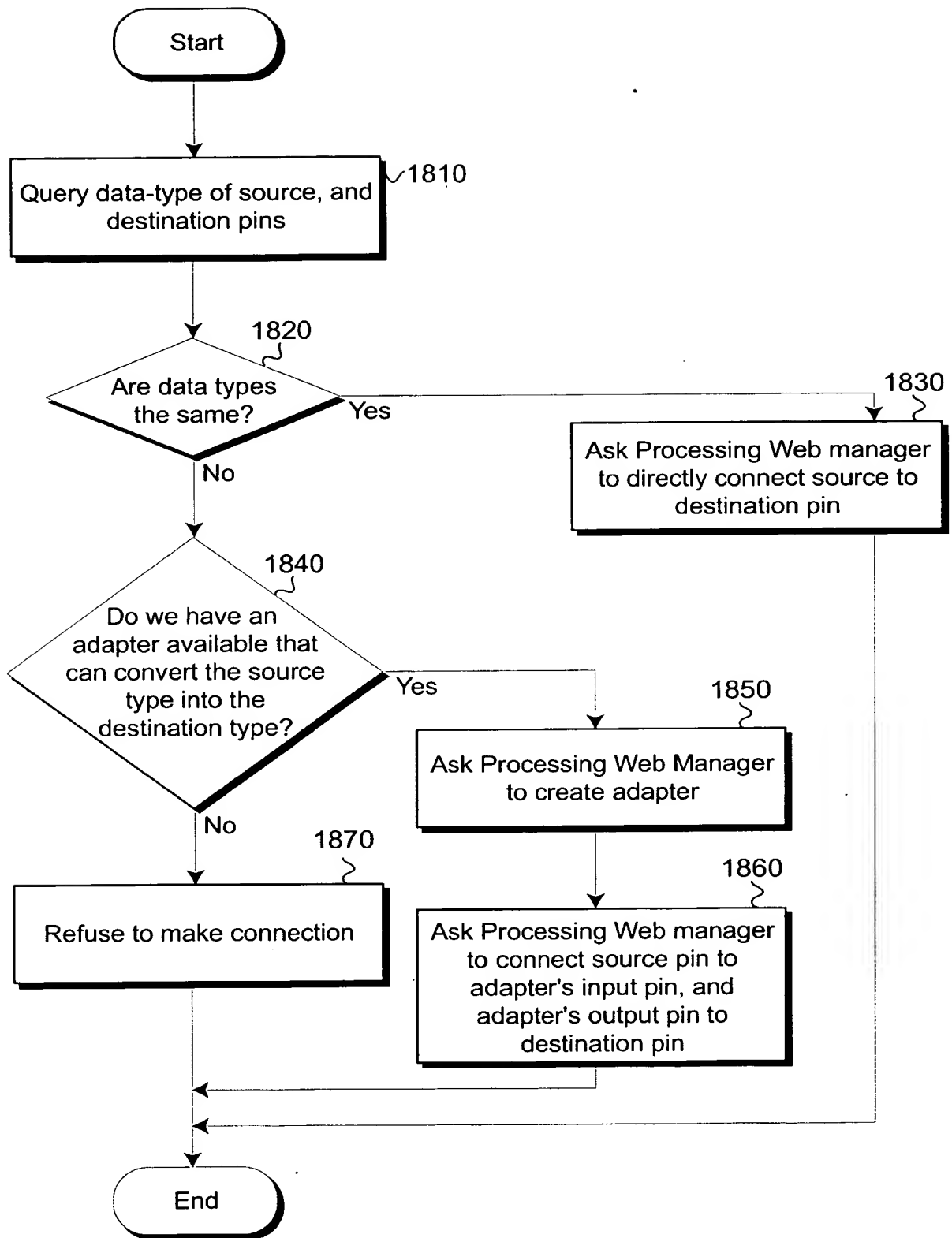


FIG. 18

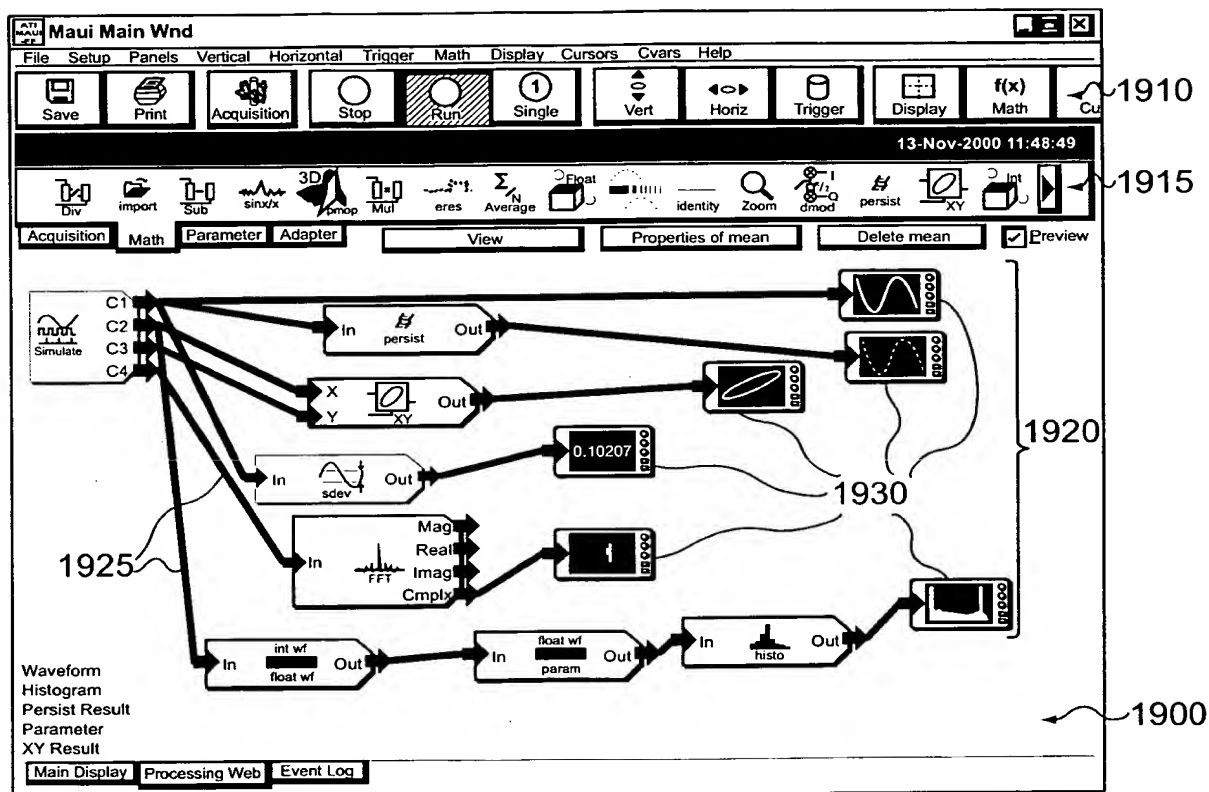


FIG. 20

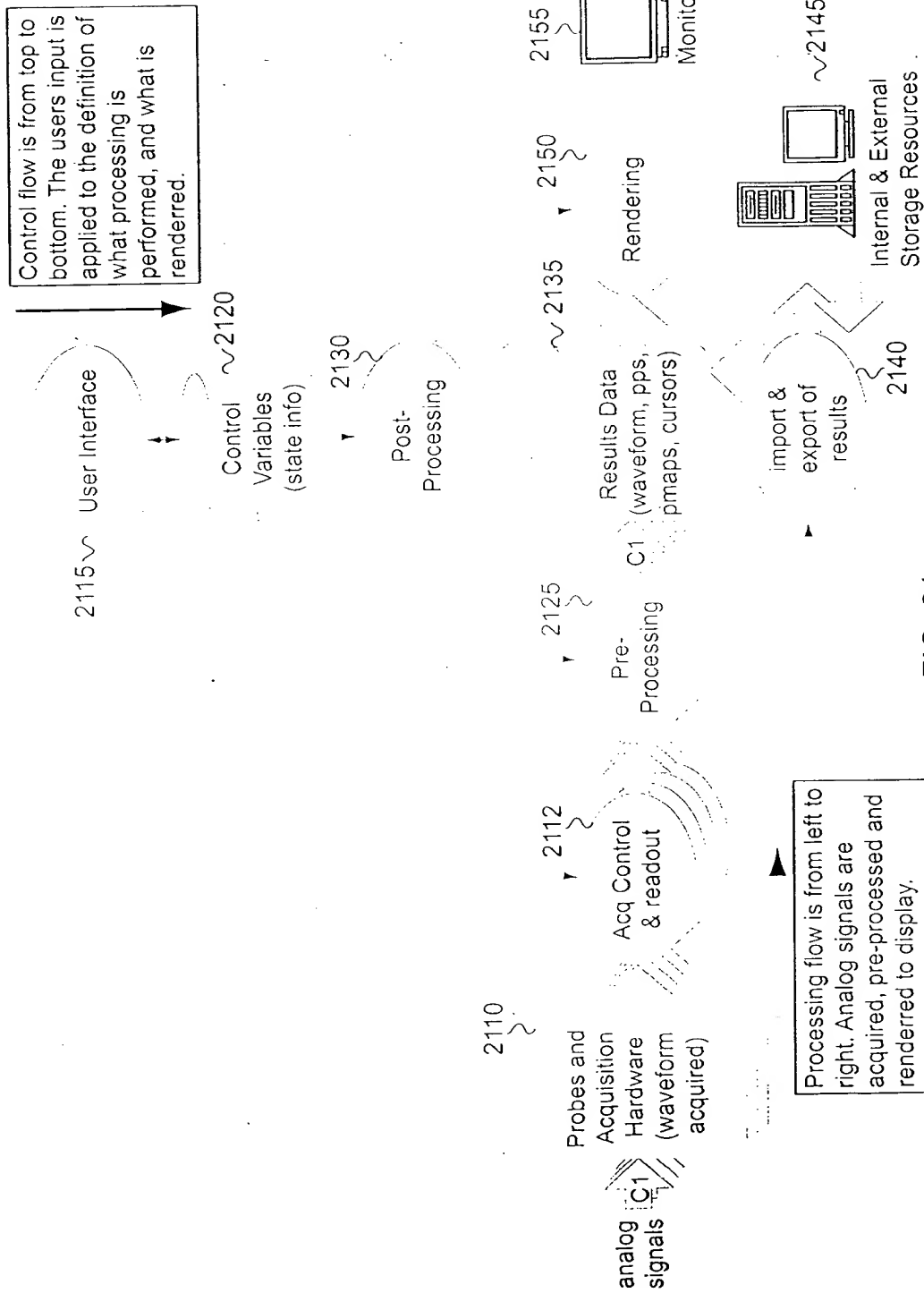


FIG. 21